

FIG. 1

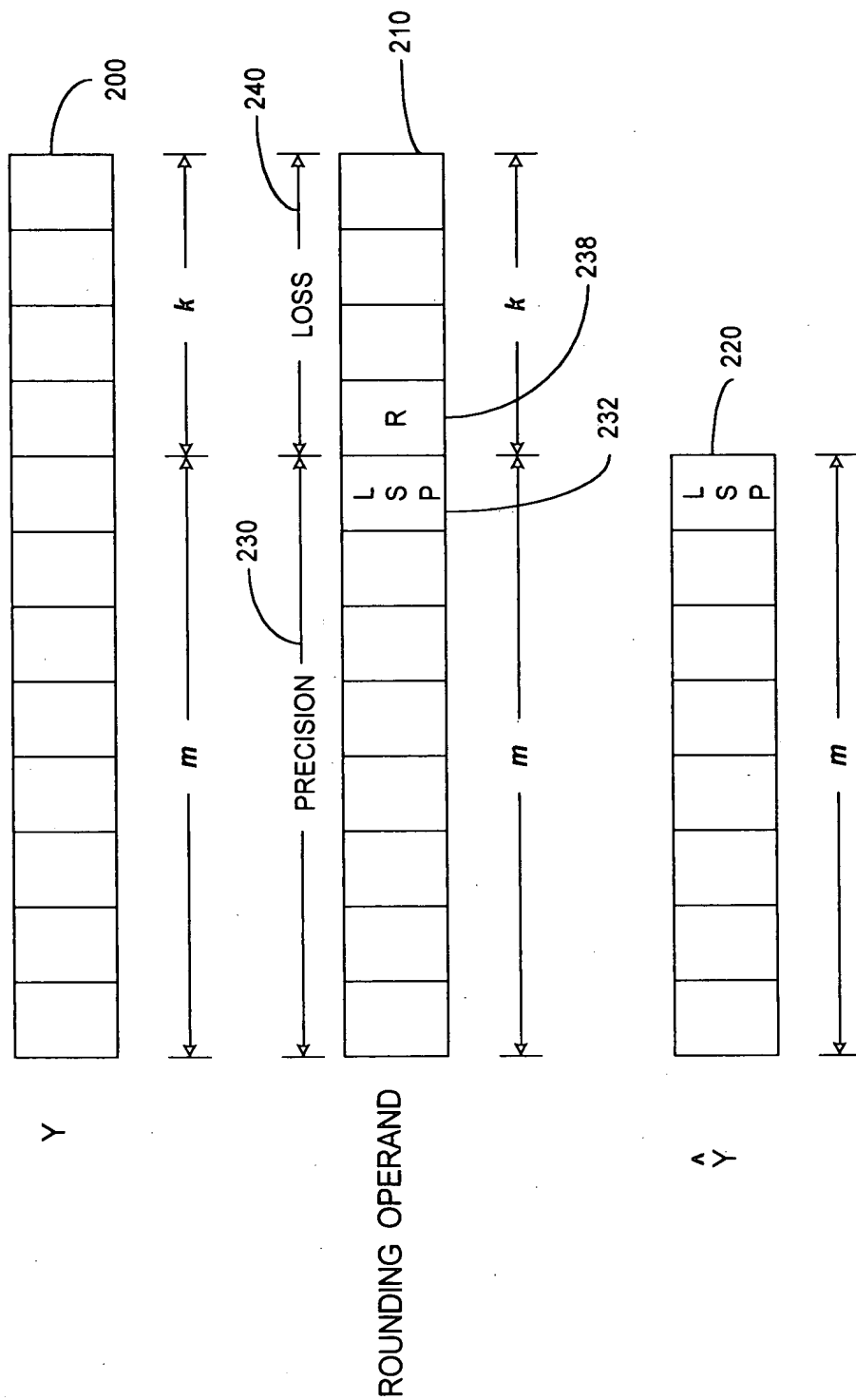


FIG. 2A

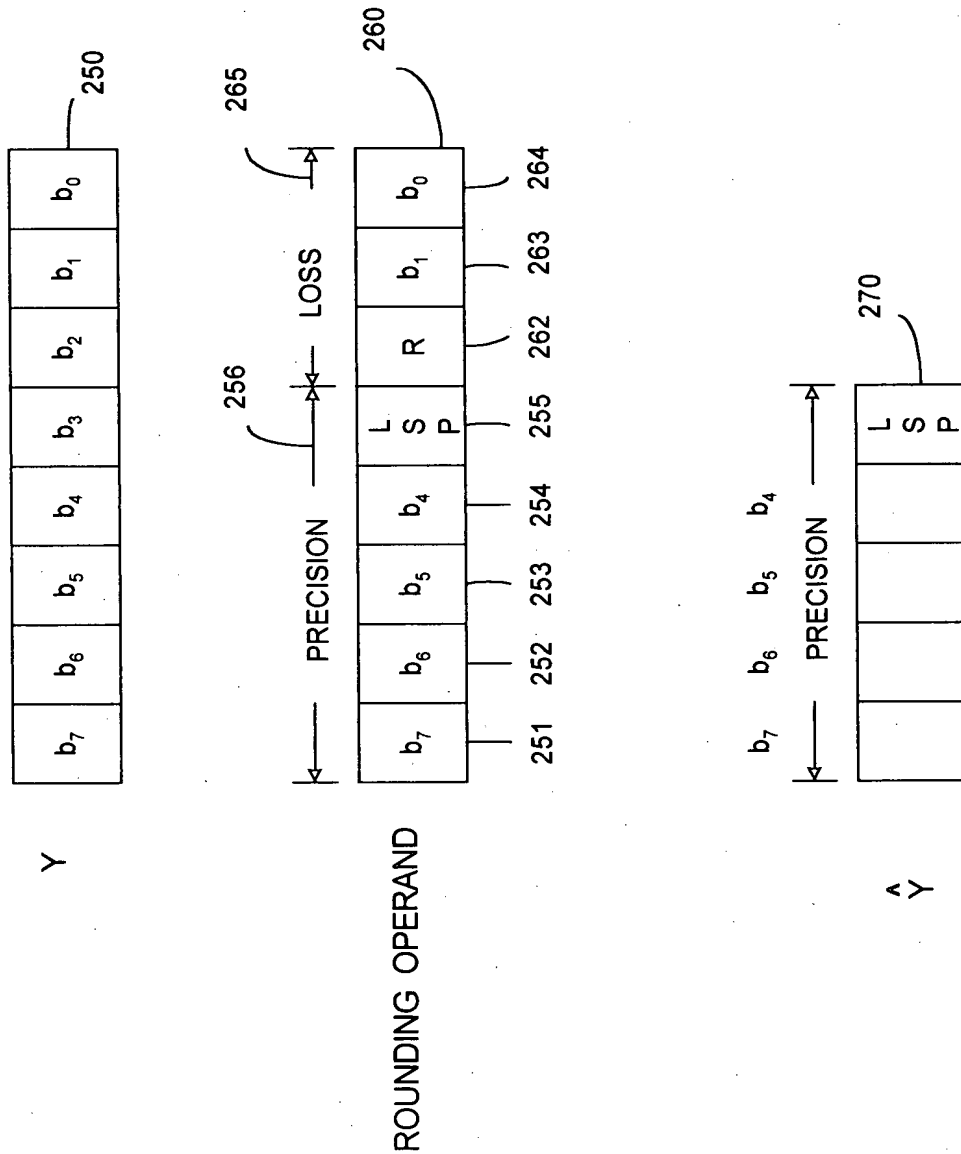


FIG. 3 is a block diagram of a system for reducing precision of data. The system includes a data source 305, a precision reduction block 310, a combiner 315, a bias generator 330, and a data output block 320. The data source 305 provides input data X to the precision reduction block 310. The precision reduction block 310 outputs data X' to the combiner 315. The combiner 315 also receives input from the bias generator 330 and outputs the final data output 320. The bias generator 330 receives feedback from the data output 320 and outputs a bias signal to the combiner 315. The system is labeled as an SBR DEVICE.

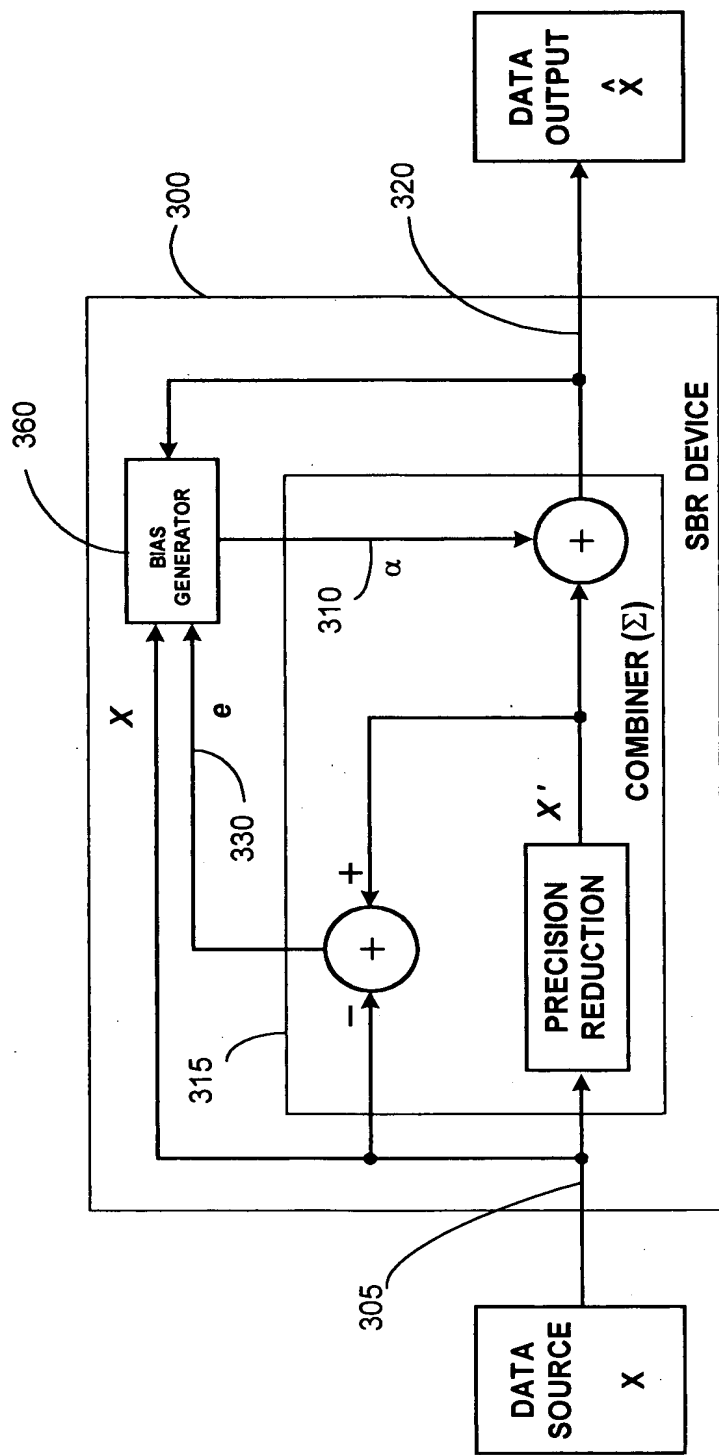


FIG. 3

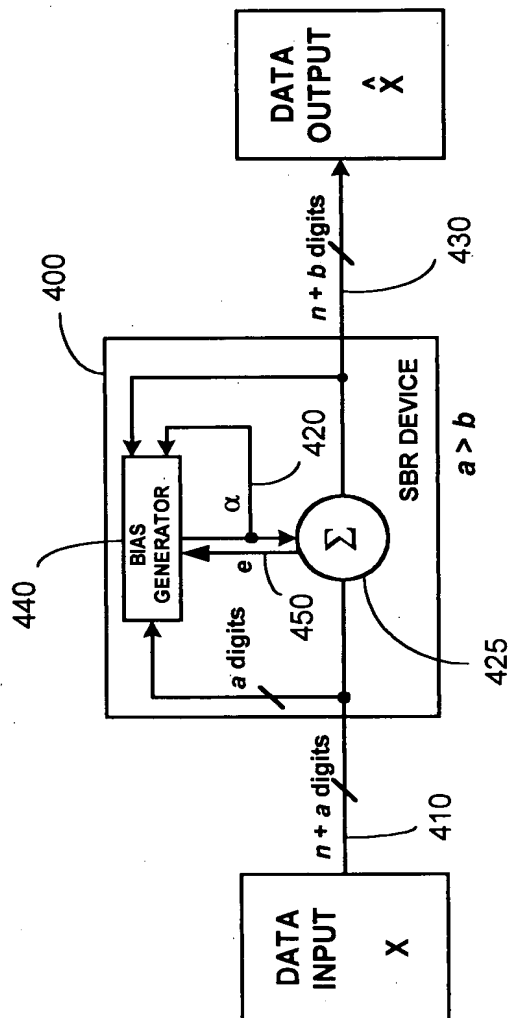


FIG. 4

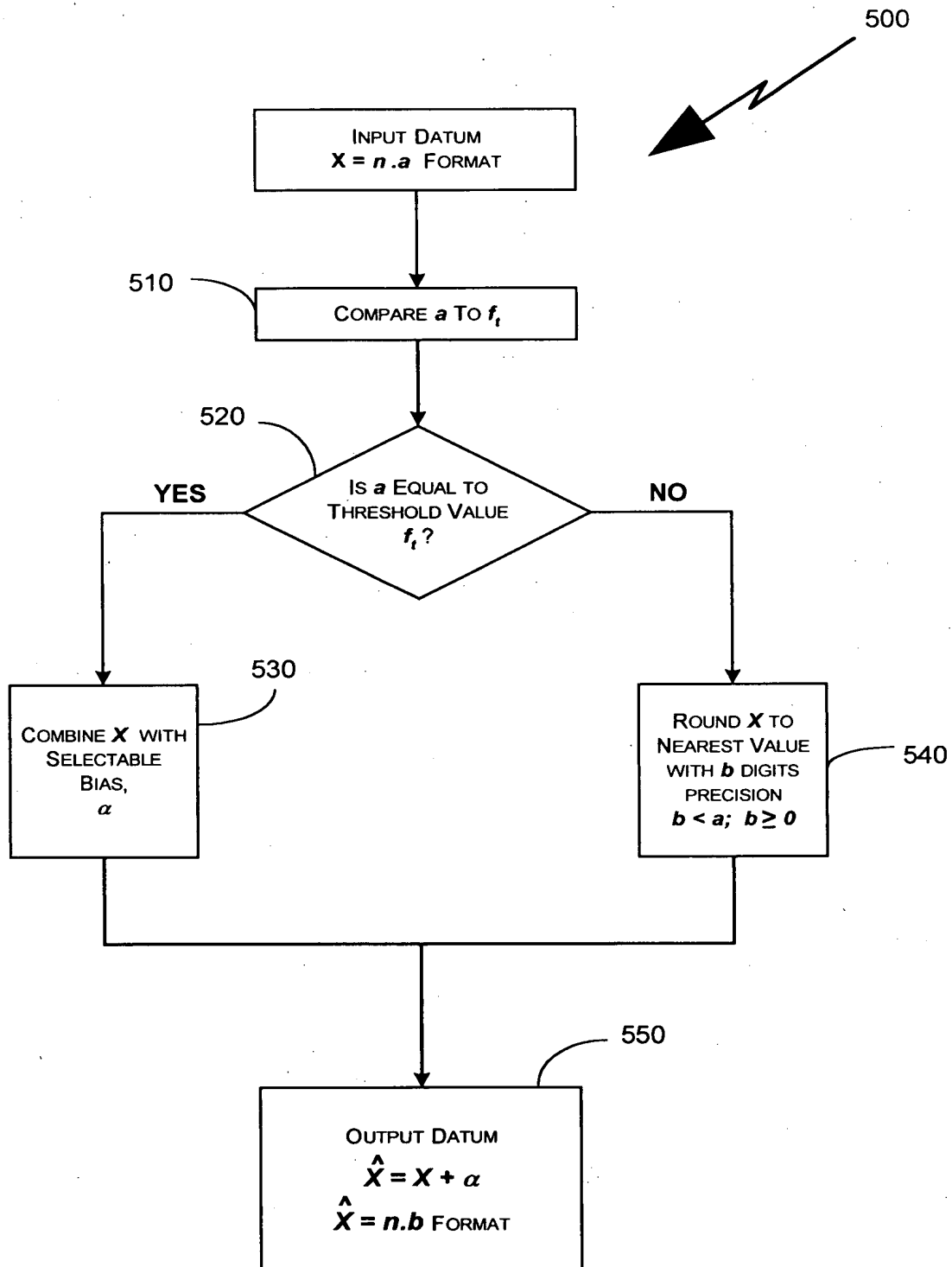


FIG. 5

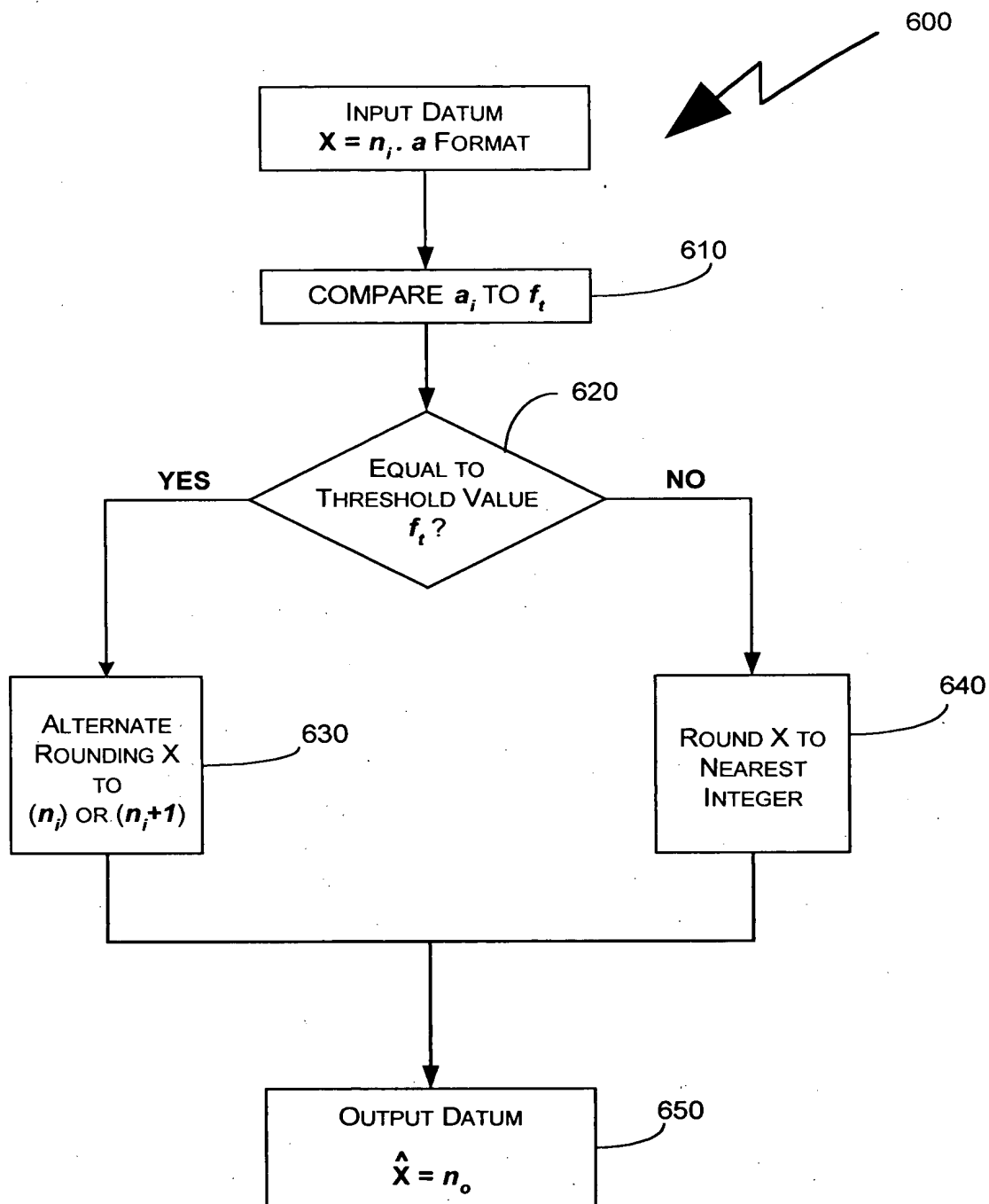


FIG. 6

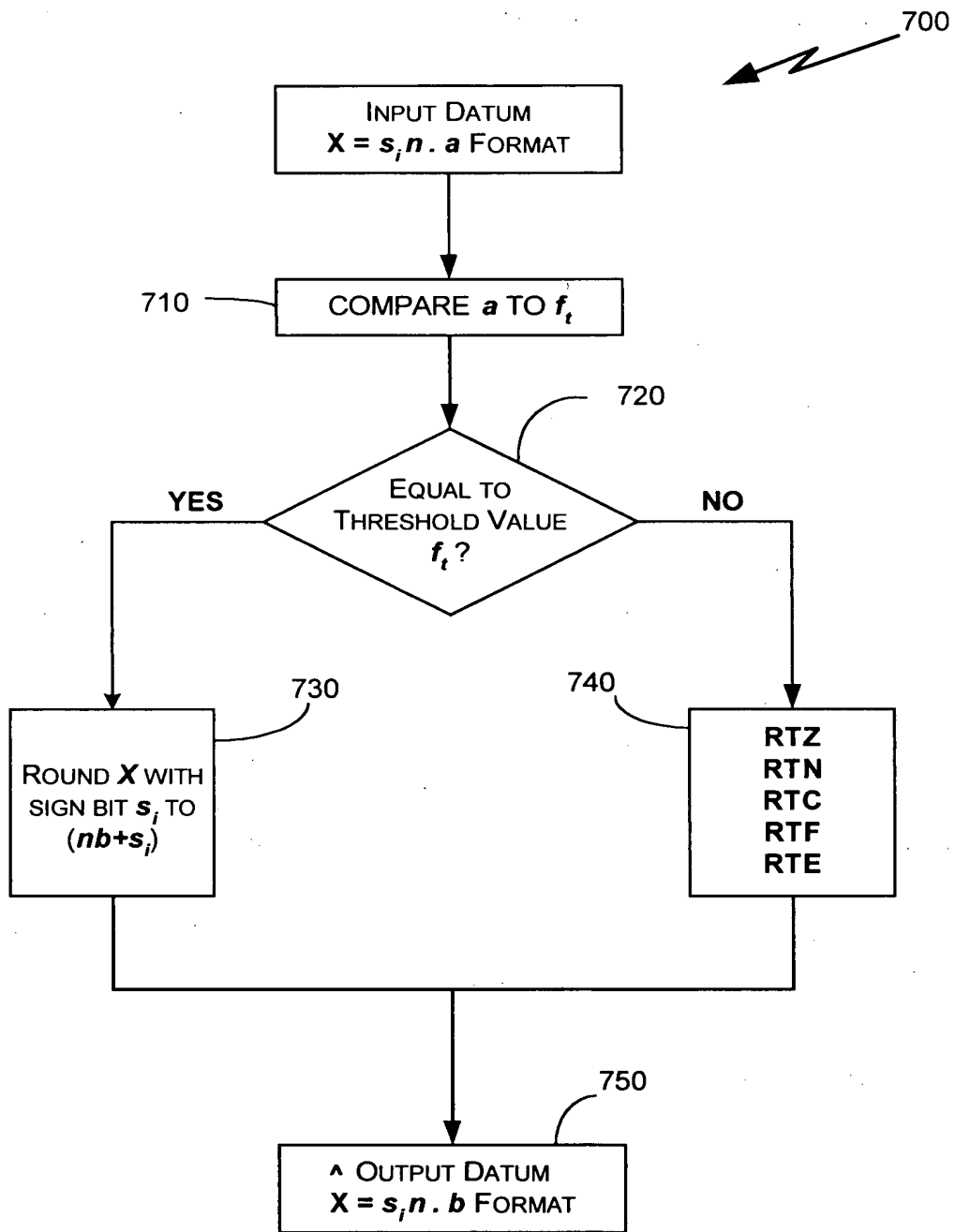


FIG. 7

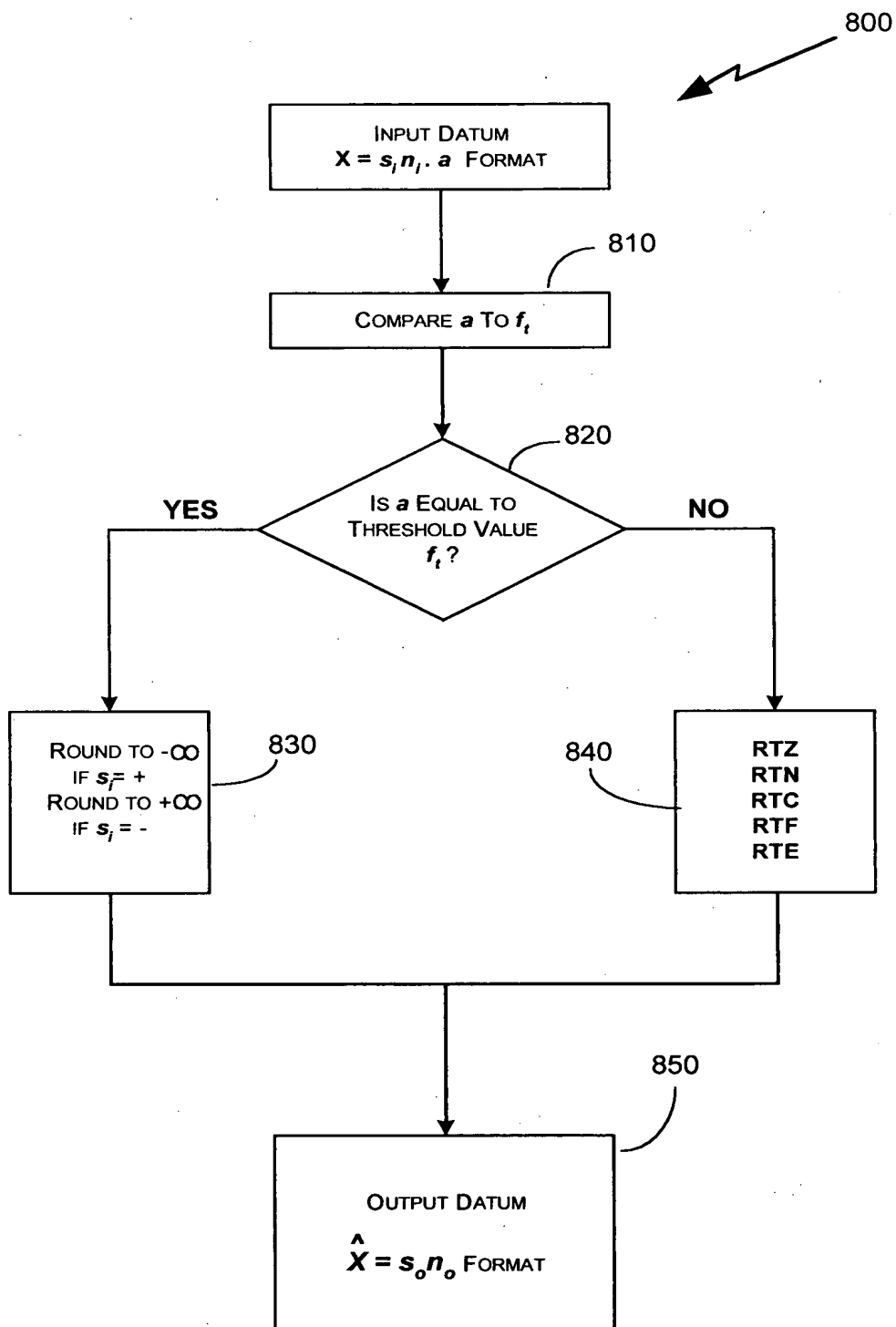


FIG. 8

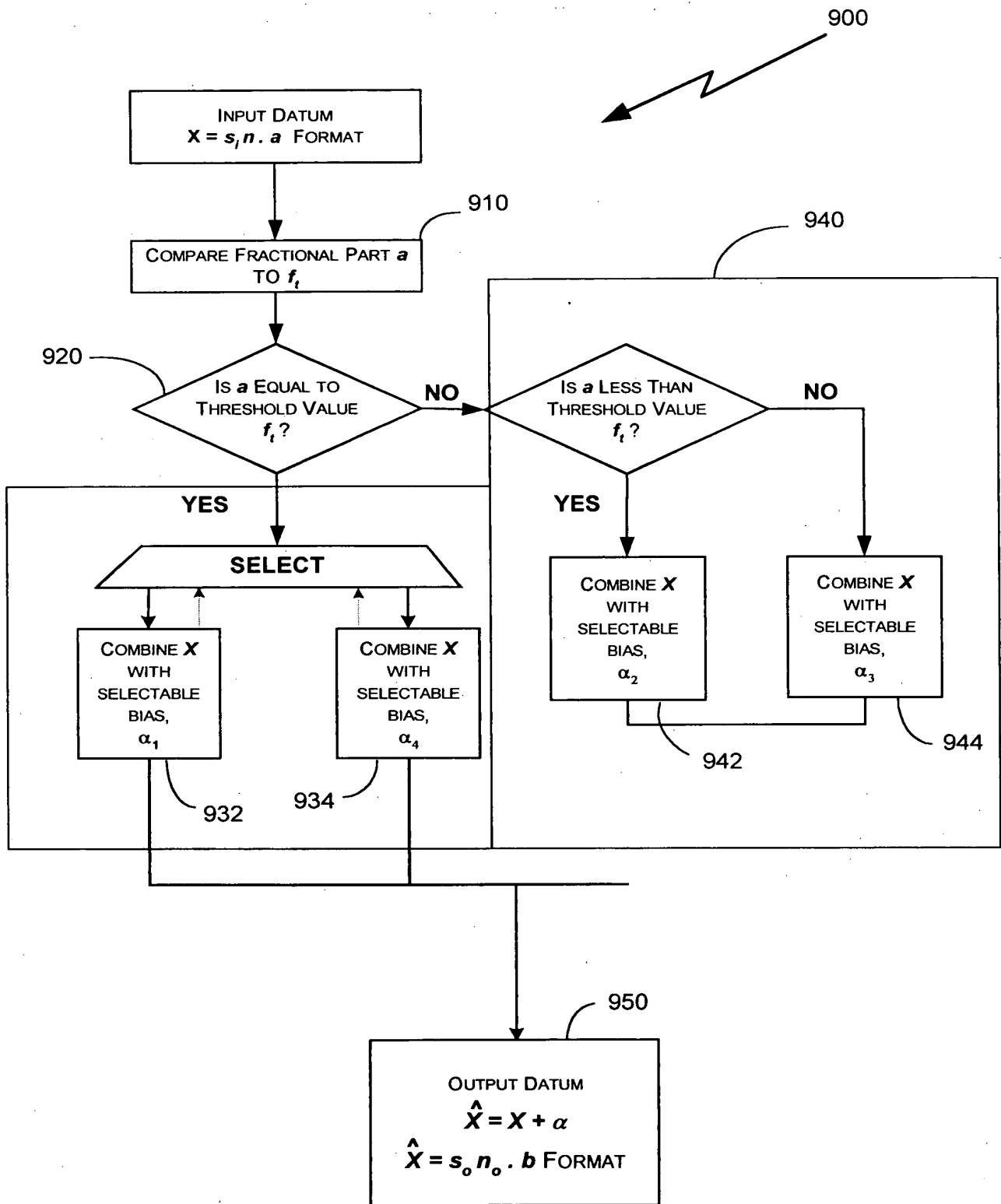


FIG. 9

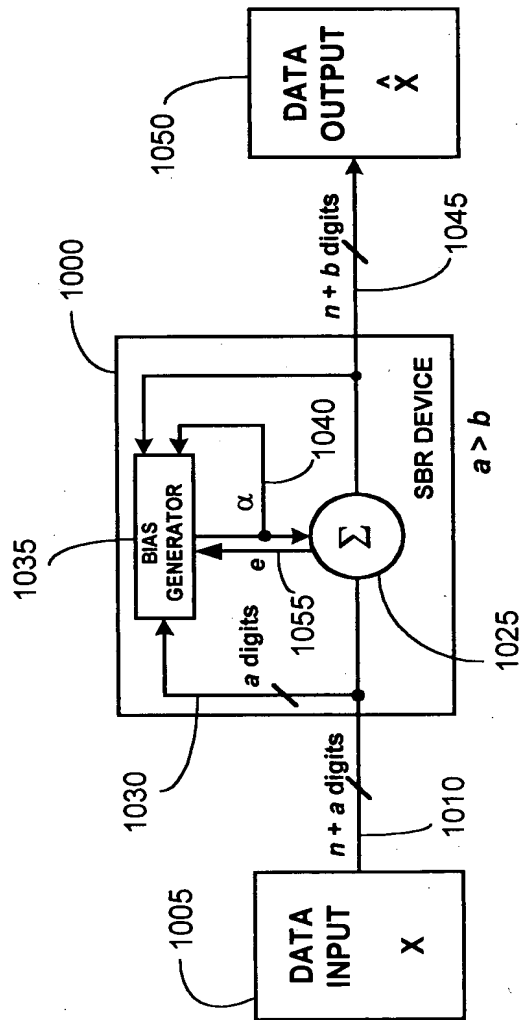


FIG. 10

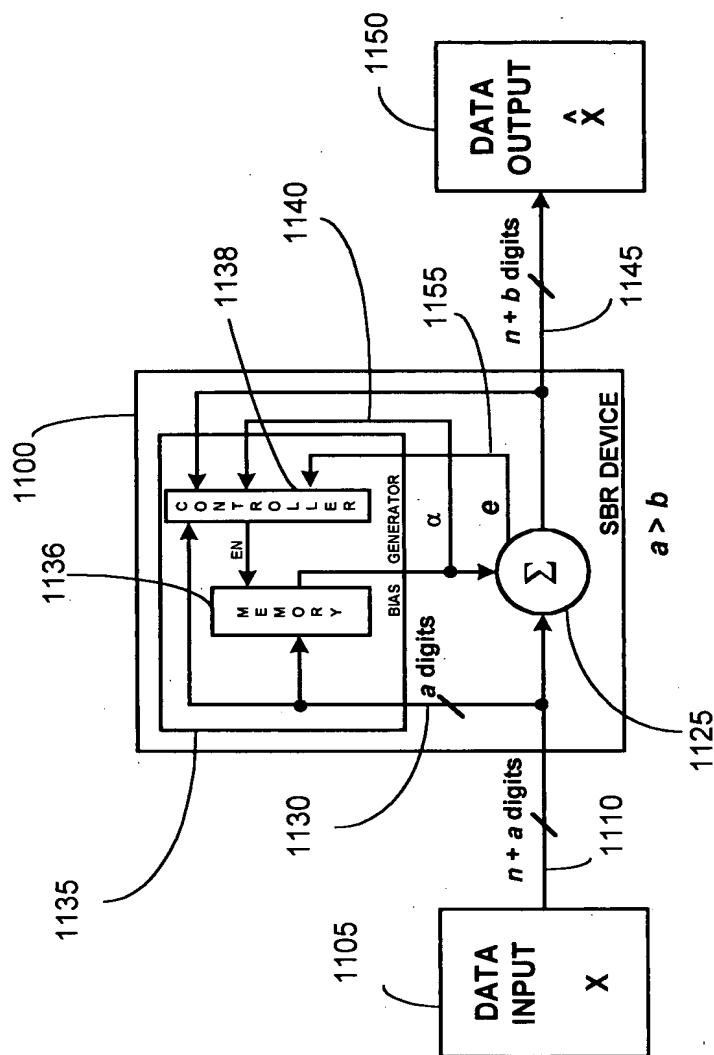


FIG. 11

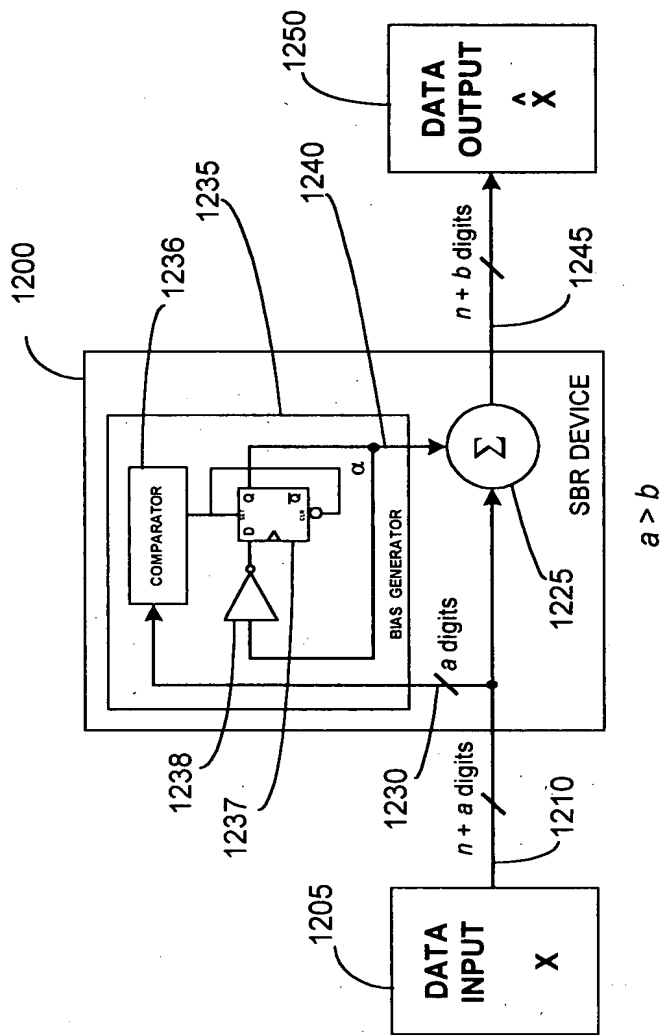


FIG. 12

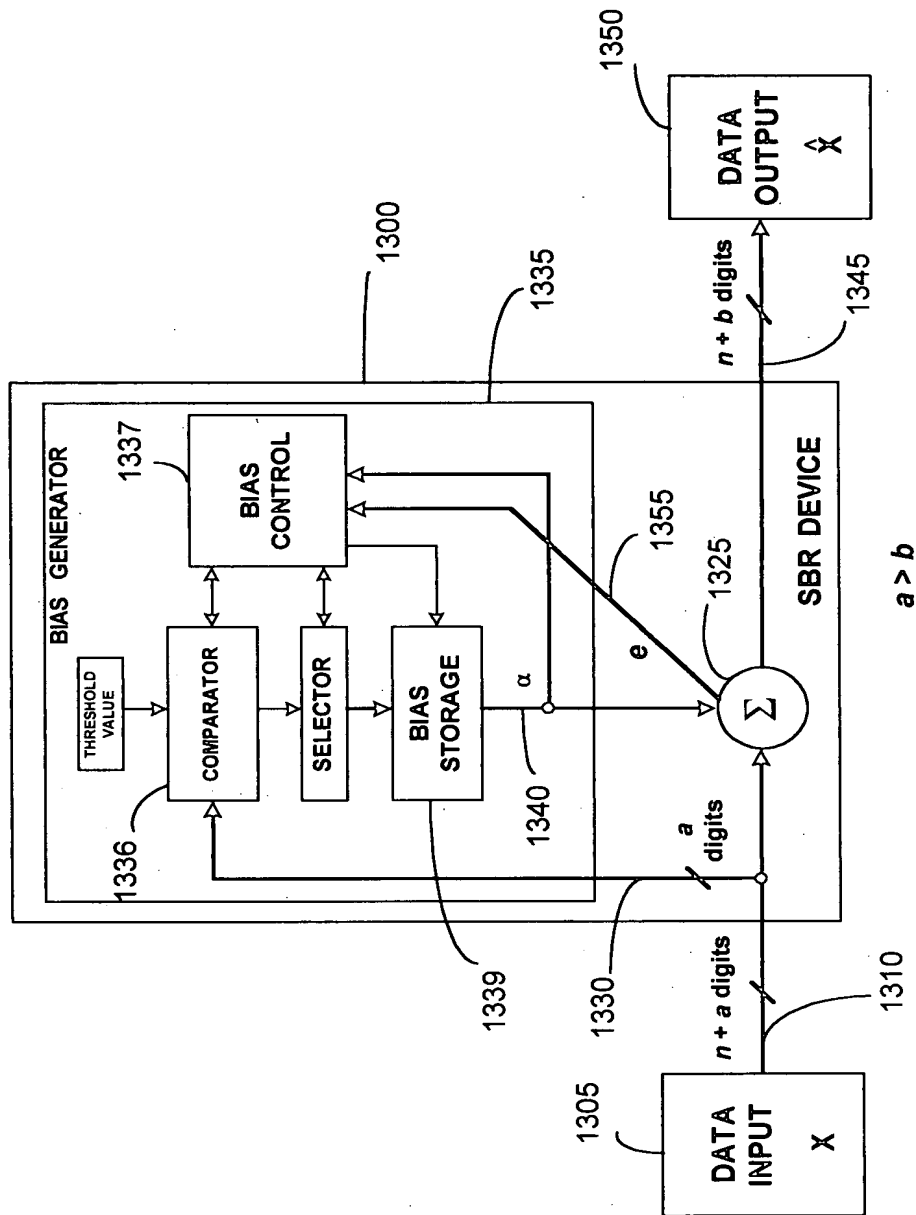


FIG. 13

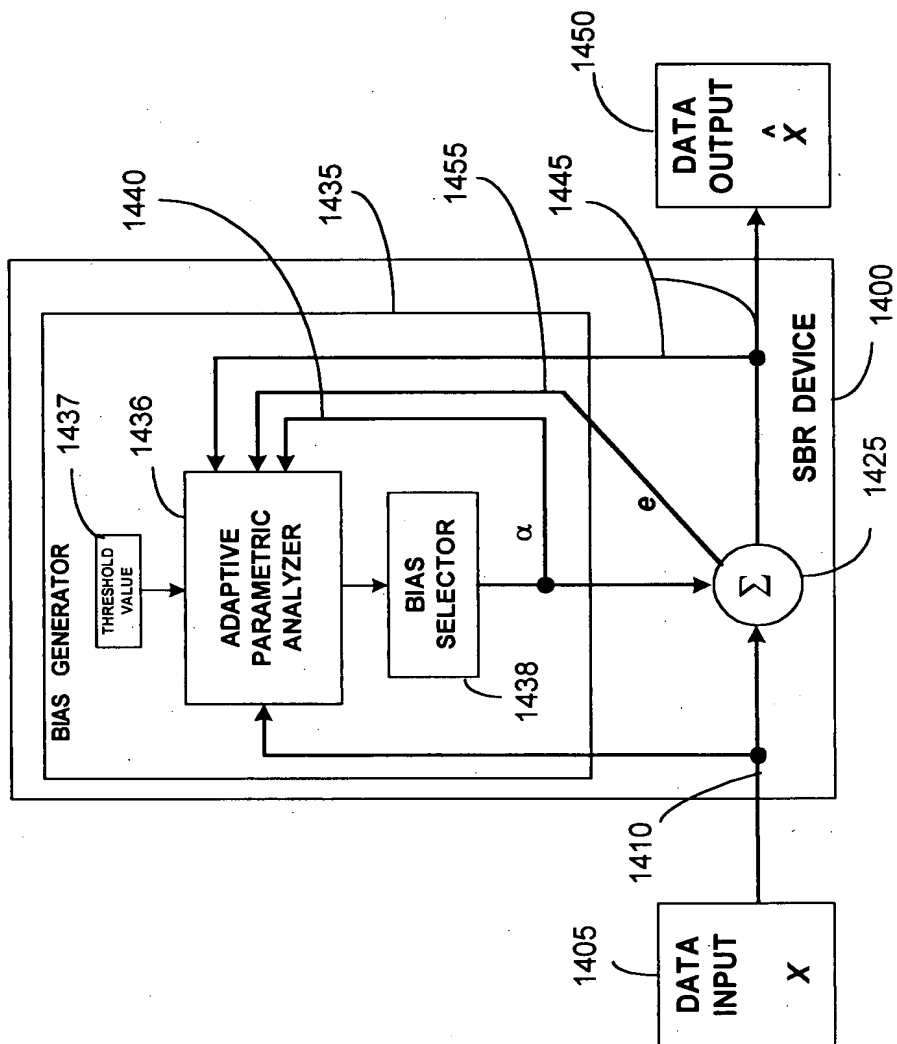


FIG. 14

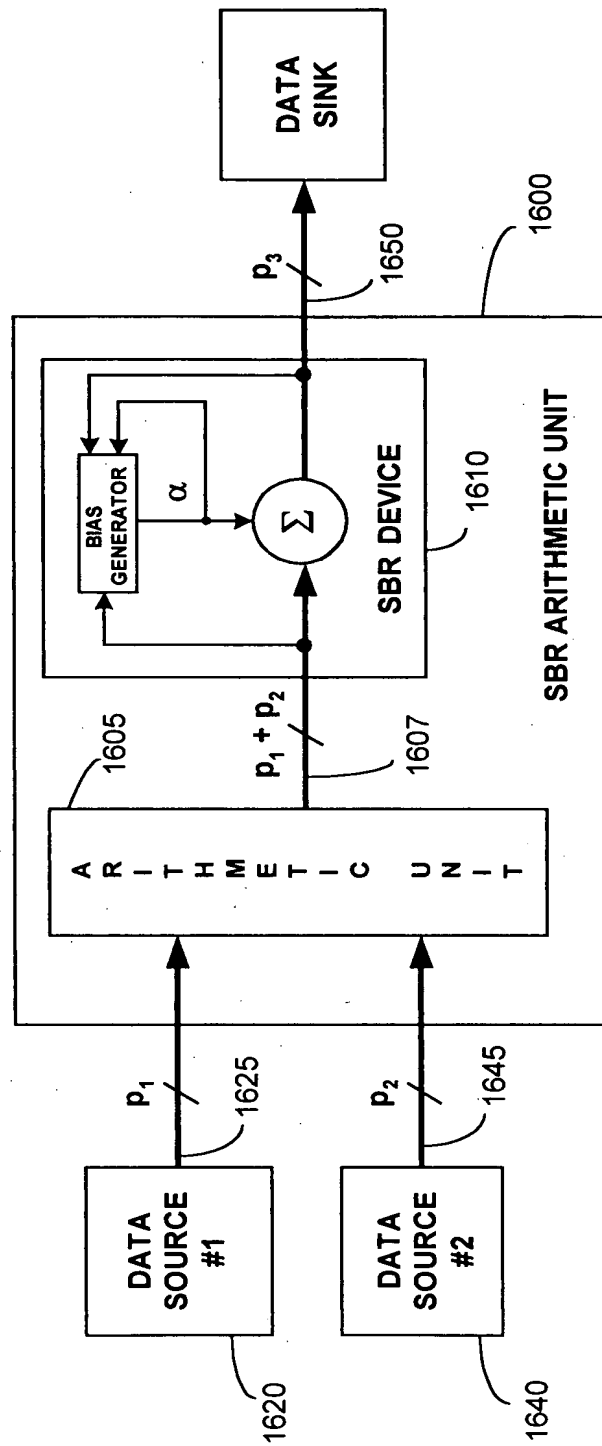


FIG. 16

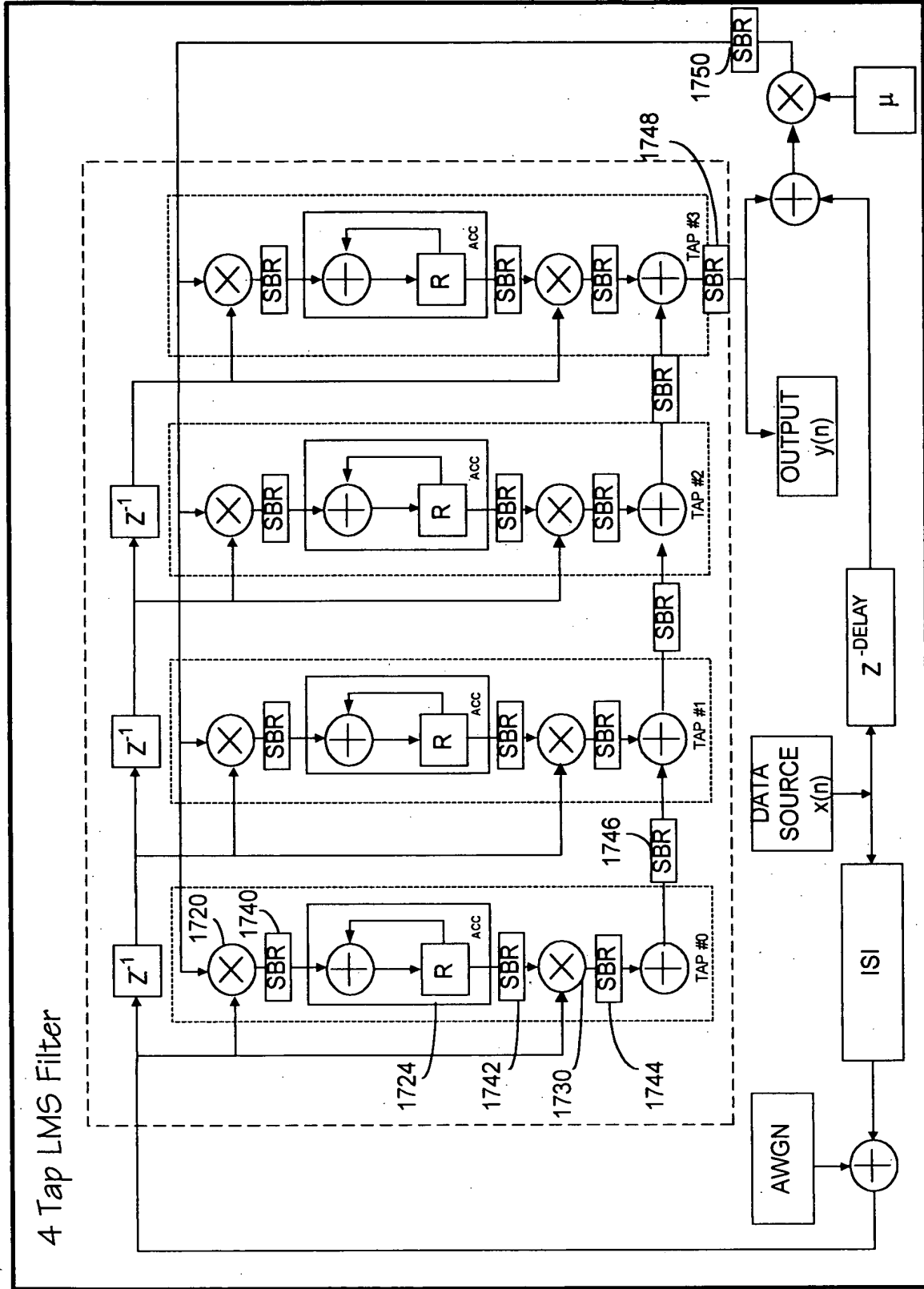


FIG. 17